**IR (Instruction register)** – Apart of the Control Unit

Current instruction to be executed or decoded.

* Instruction Code
* Volatile memory
* i.e Fetch instr from prog memory

**CU (Control Unit)** – Brains, controls all module to work together

* Process
* Non-Volatile memory
* Switches paths electrically from decoded IR instr

e.g

**IR** = Assembly receives instr **load**. Value stored in reg

**CU** = load instr is now read in binary form i.e 0001, 1 = ON, 0 = OFF